

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

88888

Applicant(s): Chih-Chieh Yeh, et al.

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Examiner:

Nguynen, Tan

Filed:

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Atty. Docket No.:

87092291.242025

For:

Nonvolatile Semiconductor Memory and Operating Method of the Memory

CERTIFICATE OF MAILING (37 C.F.R. § 1.8)

Mail Stop Issue Fee Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 I hereby certify that this paper (along with anything referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date set forth below.

Scarlett Pereida

(Name)

Date)

## **RESPONSE TO NOTICE OF ALLOWABILITY**

Sir:

In response to the Notice of Allowability November 28, 2005, Applicants hereby respectfully enclose the replacement sheets for Figure 7A, Figure 7C, Figure 7D and Figure 7F of the drawings. The notation " $V_b = V_s = -10V$  or floating" is deleted on the replacement drawings as requested by the Examiner.

This response is being filed with the Issue Fee payment of \$1,700. Applicant does not believe that any additional fees are due in connection with this filing. However, in the event that fees are required, the Commissioner is hereby authorized to charge Deposit Account No. 13-0480, referencing Attorney Docket Number 87092291.242025.

Respectfully submitted,

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